AMENDMENTS TO THE SPECIFICATION

Page 1, after the title, please insert the following NEW paragraph:

A1 This application claims priority from Provisional Application No. 60/218,571, filed July 13, 2000.

Replace the paragraph starting at page 2, line 11 with the following replacement paragraph:

A2 Attempts to utilize a 32 MHz clock for generation of the 7/60MHz TIC times, however, results in jitter due to the ~~face~~ phase differences between the specified transmit clock (e.g., 7/60MHz) and the actual transmit clock (e.g., 32 MHz). Hence, the use of an alternative transmit clock may introduce jitter that adversely affects the required low error data rate transmission.

Replace the paragraph starting at page 5, line 5, with the following replacement paragraph:

A3 The phase correction module 14 retrieves from the coding block 12 the time interval values representing transmit data as integer multiples of the normalized period 7/60 (i.e., "TIC times"), and determines a transmission time instant (i.e., the instant in time at which the waveform is to be output by the pulse position modulation communications system 10) for the prescribed waveform 32 relative to a start of frame. The phase correction module 14 also determines the phase error between the 32 MHz transmit clock 24 and the prescribed transmit clock having the normalized period of 7/60 at the transmission time instant. The phase

correction module 14 then selects samples 30 of the specified transmit pulse shape 32 that correct for the determined phase error between the 32 MHz clock and the prescribed 7/60 transmit clock at the transmission time instant by outputting to the multiplexer 18 a Table Select signal having a value based on the determined phase error, and outputting to the pulse shape tables 16 a sequence of Table Address signals starting at the transmission time instant; hence, the analog pulse waveform output after analog reconstruction by the digital to analog converter (DAC) 20 and filtering by the low pass filter 22 is a signal having substantially zero phase noise relative to the prescribed transmit clock. Hence, the phase correction module 14 can correct for detected phase errors between the actual 32 MHz transmit clock 24 [[in]] and the prescribed 7/60 transmit clock, even when the actual transmit clock is not a simple frequency multiple of the prescribed transmit clock.

A3 cont.